



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application No.: 10/023,310

Filed: December 17, 2001

**Inventor(s):**

Chetana N. Keltcher, William

Alexander Hughes, Michael T.

Clark, and Bruce R. Holloway

Title: Using Microcode to Correct  
ECC Errors in a Processor

Examiner: Torres, Joseph D.

Group/Art Unit: 2133

Atty. Dkt. No: 5500-75900

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Lawrence J. Merkel

Printed Name \_\_\_\_\_

Signature \_\_\_\_\_

Date \_\_\_\_\_

**RESPONSE TO OFFICE ACTION OF  
June 9, 2004**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of June 9, 2004, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.

07/21/2004 AMONDAF1 00000141 501505 10023310

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